

LISTING OF CLAIMS

1. (Currently Amended) A process for refreshing a dynamic random access memory, comprising:

continuously and dynamically measuring the retention time of all the memory cells of the memory, wherein continuously and dynamically measuring the retention time comprises performing retention time measurements for the cells of the memory cyclically, and tagging the cells of the memory having a lower retention time as measured in the course of a measurement cycle, and wherein regulating the refresh period of the memory comprises setting the refresh period in the course of a next measurement cycle of the tagged cells to be refreshed more often than non-tagged cells of the memory; and

~~continuously and dynamically~~ regulating the refresh period of the memory based on the result of this measurement.

2. (Currently Amended) A ~~The~~ process for refreshing a dynamic random access memory according to Claim 1, comprising:

continuously and dynamically measuring the retention time of all the memory cells of the memory, wherein continuously and dynamically measuring the retention time of all the memory cells comprises:

successively selecting different groups of cells of the memory in such a way as to scan the entire set of cells of the memory;

successively measuring the retention times of each group of cells; and

successively refreshing the unselected cells;

wherein successively measuring comprises performing measurements on a selected group of cells at a lower measurement frequency than a refresh frequency of the unselected cells of the memory such that each selected group of cells is refreshed more slowly than unselected cells of the memory; and

regulating the refresh period of the memory based on the result of this measurement.

3. (Original) The process according to Claim 2, wherein the memory is organized by pages, and each selected group of cells comprises an integer number of pages.

4. (Original) The process according to Claim 2,

wherein selectively measuring comprises:

writing predetermined test content to the selected group of cells;

reading of the selected group of cells; and

metering of a number of content errors in the reading of the selected group of cells in comparison to the predetermined test content;

wherein selectively measuring the retention time comprises measuring the retention time of all the memory cells by an at least partial accumulation of the metered number of content errors; and

wherein successively refreshing comprises refreshing the unselected cells of the memory at least twice in between the writing of the test content to the selected group of cells and the reading of the selected group of cells.

5. (Original) The process according to Claim 4, wherein successively measuring further comprises, for at least one selected group of cells, backing up of the content of the selected group of cells, and restoring of the content of the selected group of cells with the backed up content following metering of the number of content errors.

6. (Original) The process according to Claim 4,
wherein successive measuring comprises writing of the test content into a first buffer
memory before writing to the selected group of cells; and

wherein reading of the test content from the selected group of cells comprises writing of
the read content of the selected group of cells into a second buffer memory followed by reading
of the second buffer memory.

7. (Original) The process according to Claim 5, wherein backing up comprises
storing in a predetermined part of the memory or else in an external backup memory.

8. (Original) The process according to Claim 6, wherein each selected group of cells
forms a single page of the memory, and backing up comprises storing in one of the first and
second buffer memories.

9. (Original) The process according to Claim 4, wherein regulating of the refresh period of the memory comprises:

comparing the metered number of accumulated errors with a low threshold and a high threshold;

increasing the refresh period if the metered number of accumulated errors is less than the low threshold;

decreasing the refresh period if the metered number of accumulated errors is greater than the high threshold; and

non-modifying the refresh period if the metered number of accumulated errors is greater than or equal to the low threshold and less than or equal to the high threshold.

10. (Original) The process according to Claim 4, wherein regulating of the refresh period of the memory comprises:

comparing the metered number of accumulated errors with a threshold;

increasing the refresh period if the metered number of errors is less than the threshold;
and

decreasing the refresh period if the metered number of errors is greater than or equal to the threshold.

11. (Original) The process according to Claim 10, wherein refresh period has a minimum limit value and a maximum limit value.

12. (Canceled).

13. (Original) A The process for refreshing a dynamic random access memory according to Claim 1, comprising:

continuously and dynamically measuring the retention time of all the memory cells of the memory; and

regulating the refresh period of the memory based on the result of this measurement;

wherein the memory is incorporated into an apparatus which operates in both a standby mode and an active mode of operation, and wherein measuring and regulating are performed at least in the course of the standby mode.

14. (Original) The process according to Claim 13, wherein the apparatus is a handset of a wireless communication system.

15. (Currently Amended) A dynamic random access memory device, comprising:

a dynamic random access memory; and

a memory refreshing circuit that operates to continuously and dynamically measure the retention time of all the memory cells of the memory, and to ~~continuously and dynamically~~ regulate the refresh period of the memory based on the result of this measurement;

wherein the memory refreshing circuit cyclically performs the measurement of the retention time of all the cells of the memory, and tags the cells of the memory having a lower retention as measured in the course of a measurement cycle, and regulates the refresh period of the memory by setting the refresh period in the course of a next measurement cycle of the tagged cells to be refreshed more often than non-tagged cells of the memory.

16. (Currently Amended) A dynamic random access memory ~~The device according to~~
~~Claim 15, comprising:~~

a dynamic random access memory; and

a memory refreshing circuit that operates to continuously and dynamically measure the retention time of all the memory cells of the memory, and to regulate the refresh period of the memory based on the result of this measurement, wherein the memory refresh circuit comprises:

a selection circuit operating to perform successive selections of different groups of cells of the memory in such a way as to scan the entire set of cells of the memory;

a measurement circuit operating to perform successive measurements of the retention times of each selected group of cells; and

a refresh circuit operating to perform successively refresh the unselected cells;

wherein the measurement circuit is activated to perform measurements on a selected group of cells at a lower measurement frequency than a refresh frequency of the unselected cells of the memory such that selected groups of cells are refreshed more slowly than unselected cells of the memory.

17. (Original) The device according to Claim 16, wherein the memory is organized by pages, and each selected group of cells comprises an integer number of pages.

18. (Original) The device according to Claim 16, wherein the measurement circuit comprises:

a storage device to store predetermined test content;

a write circuit to write the predetermined test content to the selected group of cells;

a read circuit to read the selected group of cells;

a metering circuit to meter the number of content errors in the reading of the selected group of cells in comparison to the predetermined test content; and

an accumulation circuit to perform an at least partial accumulation of the metered number of content errors; and

wherein the refresh circuit refreshes the unselected cells of the memory at least twice in between the writing of the test content to the selected group of cells and the reading of the selected group of cells.

19. (Original) The device according to Claim 18, wherein the measurement circuit further operates, for at least one selected group of cells, to backup the content of the selected group of cells and restore the content of the selected group of cells with the backed up content following metering of the number of content errors.

20. (Original) The device according to Claim 18, further comprising:
a first and a second buffer memory connected to the dynamic random access memory;
wherein the write circuit writes the test content into the first buffer memory before writing to the selected group of cells, and wherein the read circuit writes the read content of the selected group of cells into the second buffer memory followed by reading the second buffer memory.

21. (Original) The device according to Claim 19, wherein the measurement circuit performs the backup in a predetermined part of the memory or else in an external backup memory.

22. (Original) The device according to Claim 20, wherein each selected group of cells forms a single page of the memory, and backing is made to one of the first and second buffer memories.

23. (Original) The device according to Claim 18, wherein the memory refreshing circuit comprises:

a comparison circuit to compare the metered number of accumulated errors with a low threshold and a high threshold; and

a regulating circuit that increases the refresh period if the metered number of errors is less than the low threshold, decreases the refresh period if the metered number of errors is greater than the high threshold, and leaves the refresh period unchanged if the metered number of errors is greater than or equal to the low threshold and less than or equal to the high threshold.

24. (Original) The device according to Claim 18, wherein the memory refreshing circuit comprises:

a comparison circuit to compare the metered number of accumulated errors with a threshold; and

a regulating circuit to increase the refresh period if the metered number of errors is less than the threshold and decrease the refresh period if the metered number of errors is greater than or equal to the threshold.

25. (Original) The device according to Claim 24, further comprising storage means for storing a minimum limit value and a maximum limit value for the refresh period.

26. (Canceled).

27. (Currently Amended) A dynamic random access memory ~~The device according to Claim 15, comprising:~~

a dynamic random access memory; and

a memory refreshing circuit that operates to continuously and dynamically measure the retention time of all the memory cells of the memory, and to regulate the refresh period of the memory based on the result of this measurement;

wherein the device is incorporated into an apparatus which operates in both a standby mode and an active mode of operation, and wherein the memory refreshing circuit operates to measure and regulate during at least the course of the standby mode.

28. (Original) The device according to Claim 27, wherein the apparatus comprises a handset of a wireless communication system.

29. (Original) The device according to Claim 28, wherein the handset is a cellular mobile telephone.

30. (Original) A process for dynamically adjusting the refresh rate of a dynamic random access memory array, comprising:

selecting a group of memory cells within the memory, the selected group of memory cells comprising a sub-set of the entire memory array;

measuring a test data content retention time for the selected group of memory cells at a measurement rate which exceeds a current refresh rate of the dynamic random access memory array;

in the meantime, continuing to refresh the non-selected memory cells within the memory at the current refresh rate; and

adjusting the current refresh rate based on the measured test data content retention time.

31. (Original) The process according to Claim 30, further comprising repeating the steps of claim 30 and selecting a different group of memory cells with each repeat so as to scan all of the cells of the memory.

32 (Original) The process according to Claim 31, wherein adjusting comprises making an adjustment to the current refresh rate based on the measured test data content retention times for all groups of memory cells.

33. (Original) The process according to Claim 30, wherein the measurement rate is selected in comparison to the current refresh rate so as to refresh the non-selected memory cells at least twice before a test data content retention time measurement is made.

34. (Original) The process according to Claim 30, wherein measuring comprises:
writing test data to the selected group of memory cells;
reading the test data from the selected group of memory cells after expiration of a delay
set by the measurement rate;
counting a number of content errors in the read test data.

35. (Previously Presented) The process according to Claim 34, wherein adjusting
comprises:
comparing the number of content errors to a threshold;
decreasing the current refresh rate if the number is less than the threshold; and
increasing the current refresh rate if the number more than the threshold.

36. (Original) The process according to Claim 34, further comprising repeating the
steps of claim 30 and selecting a different group of memory cells with each repeat so as to scan
all of the cells of the memory, and wherein counting comprises accumulating the number of
content errors with each repeat.

37. (Previously Presented) The process according to Claim 36, wherein adjusting comprises:

comparing the accumulated number of content errors to a threshold;

decreasing the current refresh rate if the accumulated number is less than the threshold;

and

increasing the current refresh rate if the accumulated number more than the threshold.

38. (Original) A process for selectively adjusting refresh rate of a dynamic random access memory array, comprising:

successively selecting groups of memory cells within the memory, each selected group of memory cells comprising a sub-set of the entire memory array;

measuring a test data content retention time for each selected group of memory cells;

in the meantime, continuing to refresh the non-selected memory cells in accordance with their refresh rate;

tagging certain ones of the selected groups of cells having a lower test data retention time; and

adjusting the refresh rate for the memory cells such that tagged selected groups of memory cells are refreshed more frequently than non-tagged selected groups of memory cells.

39. (Original) The process according to Claim 38, wherein measuring comprises:

writing test data to the selected group of memory cells;

reading the test data from the selected group of memory cells after expiration of a delay;

counting a number of content errors in the read test data.

40. (Original) The process according to Claim 39, wherein tagging identifies certain ones of the selected groups of cells whose number of content errors exceeds a threshold.

41. (Original) The process according to Claim 38, further comprising regulating the refresh rate for non-tagged selected groups of memory cells based on the measured test data content retention times for all selected group of cells.

42. (Previously Presented) The process according to Claim 41, wherein regulating comprises:

- writing test data to each selected group of memory cells;
- reading the test data from each selected group of memory cells after expiration of a delay;
- accumulating a number of content errors in the read test data;
- comparing the accumulated number of content errors to a threshold;
- decreasing the refresh rate for non-tagged selected groups of memory cells if the accumulated number is less than the threshold; and
- increasing the refresh rate for non-tagged selected groups of memory cells if the accumulated number more than the threshold.